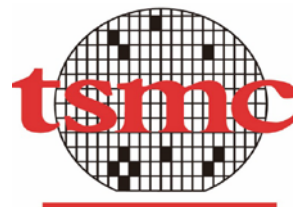


Расписание запусков прототипирования (MPW) TSMC во втором полугодии 2015 г.



www.km211.com

+7 (499) 940-03-56
Дмитрий Пустов
dp@km211.com



КМ211 - официальный представитель TSMC в России.

TSMC — крупнейший мировой контрактный
производитель полупроводниковых микросхем

Техпроцесс	Длительность*	Дата запуска (Часовой пояс TWN)					
		Июль	Август	Сентябрь	Октябрь	Ноябрь	Декабрь
16 nm	128	Jul-29					Dec-8
Logic, Fin FET Plus (0.8/1.8V)		V					V
28 nm	73-80	Jul-15	Aug-19	Sep-16	Oct-21	Nov-18	Dec-16
Logic, HPM (0.9/1.8V, 0.9/2.5V)		V	V	V	V	V	V
Logic, HPC (0.9/1.8V, 0.9/2.5V)		V	V	V	V	V	V
Logic, HPC Plus (0.9/1.8V, 0.9/2.5V)		V	V	V	V	V	V
Logic, HP (0.85/1.8V, 0.85/2.5V)		V	V	V	V	V	V
Logic, LP (1.05/1.8V, 1.05/2.5V)		V	V	V	V	V	V
Logic, LP Plus (1.05/1.8V, 1.05/1.05/1.8V)		V	V	V	V	V	V
Logic, LP-RF (1.05V/1.8V)		V	V	V	V	V	V
Logic, ULP (0.7/1.8V)		V		V		V	
Logic, HPL (1.0&1.8V, 1.0/2.5V)		V	V	V	V	V	V
Logic, HPL-RF (1.0&1.8V, 1.0/2.5V)		V	V	V	V	V	V
40nm LP & 45nm GS (45GS = 40G)	77	Jul-8	Aug-5	Sep-2	Oct-14	Nov-4	Dec-2
Logic, 40LP (1.1/1.8V, 1.1/2.5V)		V	V	V	V	V	V
Logic, 40LP Plus (1.1/2.5V)		V	V	V	V	V	V
Logic, 40ULP (1.1/2.5V, 0.9/2.5V)		V	V	V	V	V	V
Mixed-Signal/RF, 40LP (1.1/1.8V, 1.1/2.5V)		V	V	V	V	V	V
Mixed-Signal/RF, 40ULP (1.1/2.5V, 0.9/2.5V)		V	V	V	V	V	V
Logic, 45GS (45GS = 40G) (0.9/1.8V, 0.9/2.5V)		V	V	V	V	V	V
55 nm	65-82	Jul-8	Aug-3	Sep-9	Oct-14	Nov-11	Dec-9
Logic, G Plus (1.0/1.8V, 1.0/2.5V)		V	V	V	V	V	V
Logic, LP (1.2/1.8V, 1.2/2.5V)		V	V	V	V	V	V
Logic, ULP (0.9V/2.5V)		V	V		V		V
Mixed-Signal/RF, LP(1.2/1.8V, 1.2/ 2.5V)		V	V	V	V	V	V
Mixed Signal/RF, ULP (0.9V/2.5V)		V	V		V		V
EmbFlash (1.2/2.5V)			V				V
EmbFlash, ULP (0.9&2.5V)			V				V
High Voltage (1.2/6/32V)				V			V
65 nm	63	Jul-8/22	Aug-12/26	Sep-9/23	Oct-14/28	Nov-11/25	Dec-9/23
Logic, LP/DGO (1.2/2.5V,1.2/3.3V)		V	V	V	V	V	V
Logic, G Plus/DGO (1.0/1.8V, 1.0/2.5V, 1.0/3.3V)		V	V	V	V	V	V
Logic, LP-based TGO (1.0/1.2/2.5V)			Aug-12		Oct-28	Nov-11	
Logic, ULP (1.0/2.5V)		V	V	V	V	V	V
Mixed-Signal/RF, GP(1.0/ 2.5V)		V	V	V	V	V	V
Mixed-Signal/RF, LP(1.2/ 2.5V)		V	V	V	V	V	V
80 nm	61-86			Sep-2			
Logic, GC (1.0/2.5V, 1.0/3.3V)				V			
Logic, High Performance (1.2/2.5V)				V			
Mixed-Signal, GC (1.0/2.5V, 1.0/3.3V)				V			
High Voltage (1.2/5/6/32V)				V			
85 nm	59		Aug-19			Nov-18	
Logic, G (1.0/1.8V, 1.0/2.5V, 1.0/3.3V)			V			V	
Logic, LP (1.2/2.5V, 1.2/3.3V)			V			V	
Logic, ULP (1.0/2.5V)			V				
Mixed-Signal/RF, LP (1.2/3.3V)			V				
90 nm	61-86	Jul-15	Aug-19	Sep-16	Oct-21	Nov-18	Dec-16
Logic, G (1.0/1.8V, 1.0/2.5V, 1.0/3.3V, 1.0/1.8/3.3V)		V	V	V	V	V	V
Logic, LP (1.2/2.5V, 1.2/3.3V)		V	V	V	V	V	V
Logic, GT (High Performance) (1.2/2.5V)		V	V	V	V	V	V
Mixed-Signal/RF,G(1.0/1.8V, 1.0/2.5V, 1.0/3.3V, 1.0/1.8/3.3V)		V	V	V	V	V	V
Mixed-Signal/RF, LP (1.2/2.5V, 1.2/3.3V)		V	V	V	V	V	V
EmbFlash, LP (1.2/3.3V)		V		V		V	
0.11 um	55-57	Jul-1			Oct-7		
Logic, G (1.2/3.3V), FSG		V					
Mixed-Signal, G (1.2/3.3V), FSG		V					
Mixed-Signal, G (1.2/3.3V), FSG, Hybrid					V		
Mixed-Signal, LP (1.2/3.3V), FSG, Hybrid					V		
0.12 um	54-56		Aug-26				
Logic, G (1.2/3.3V), FSG			V				
0.13 um	54-76	Jul-22	Aug-26	Sep-23	Oct-28	Nov-25	Dec-23
Logic, G (1.2/2.5V,1.2/3.3V); FSG		V	V	V	V	V	V
Logic, LP (1.5/2.5V,1.5/3.3V); FSG		V	V	V	V	V	V
Logic, LV (1.0/2.5V,1.0/3.3V); FSG		V	V	V	V	V	V
Mixed-Signal/RF, G (1.2/2.5V,1.2/3.3V); FSG		V	V	V	V	V	V
Mixed-Signal, LP (1.5/3.3V); FSG			V	V		V	V
High Voltage, BCD (1.5/5/10/20/28/36V)		V	V	V	V		V
High Voltage, BCD (1.5/3.3/5/10/20/28/36V)		V	V	V	V		V

0.152um	38-44	Jul-15		Sep-16		Nov-4	
Logic, G (1.8/3.3V)		V		V		V	
Mixed-Signal, G (1.8/3.3V)		V		V		V	
Mixed-Signal GPIIA, G (1.8/5V)				V		V	
0.15/0.16 um	38-44		Aug-5				
0.15um Logic, G (1.5/3.3V)							
0.15um Logic, LV (1.2/3.3V)			V				
0.16um Logic, G (1.8/3.3V)			V				
0.16um Logic, G (1.8/3.3V), Embedded OTP							
0.16um Mixed-Signal, G (1.8/3.3V)			V				
0.16um Mixed-Signal, G (1.8/3.3V), Embedded OTP							
0.16um Mixed-Signal, LP (1.8/3.3V)							
0.16um Mixed-Signal/RF, G (1.8/3.3V)			V				
0.18 um	42-75	Jul-8/15	Aug-12/19	Sep-9/16	Oct-14/21	Nov-11/18	Dec-9/16
Mixed-Signal/RF, G (1.8/3.3V)		Jul-8/15/22	Aug-12/19/26	Sep-9/16/23	Oct-14/21/28	Nov-11/18/25	Dec-9/16/23
Mixed-signal/RF, G (1.8/3.3V) embedded MTP		Jul-8/15	Aug-12/19	Sep-16	Oct-14/21	Nov-11/18	
Mixed-signal/RF, G (1.8/3.3V) embedded OTP (eMemory)		Jul-22	Aug-26		Oct-28		Dec-23
Mixed-signal/RF, G (1.8/3.3V) embedded OTP (Kilopass/eMemory)		Jul-8/15	Aug-12/19	Sep-16	Oct-14/21	Nov-11/18	
Mixed Signal, G, (1.8/5V)		Jul-8	Aug-19	Sep-9/16	Oct-14	Nov-18	Dec-9/16
Logic, G (1.8/3.3V)		Jul-8/15/22	Aug-12/19/26	Sep-9/16/23	Oct-14/21/28	Nov-11/18/25	Dec-9/16/23
Logic, G (1.8/3.3V), Embedded OTP/ MTP		Jul-8/15	Aug-12/19		Oct-14/21	Nov-11	
Logic, LV (1.5/3.3V)		Jul-15/22	Aug-12/26	Sep-9/16	Oct-21/28	Nov-11	Dec-9/23
Logic, LP (1.8/3.3V)		Jul-8	Aug-19	Sep-16	Oct-14	Nov-18	Dec-16
Logic, LP (1.8/3.3V, 1.8/5V)		Jul-22	Aug-26	Sep-23	Oct-28		Dec-23
SiGe BiCMOS, G (1.8/3.3V)			Aug-12			Nov-11	
CMOS Image Sensor (1.8/3.3V)		Jul-8					
EmbFlash(1K,20K) (1.8/3.3V)		Jul-15		Sep-9		Nov-25	Dec-9
EmbFlash Enhanced (1.8/3.3V)		Jul-15/22		Sep-9/23	Oct-21/28	Nov-25	Dec-9
EmbFlash Enhanced (1.8/5V)		Jul-15		Sep-9			Dec-9
EmbFlash HDR (1.8/3.3V)		Jul-15		Sep-9			Dec-9
EmbFlash eLL(1.8/3.3V)		Jul-15					
High Voltage (1.8/3.3/32V)			Aug-19		Oct-14	Nov-18	
High Voltage (1.8/5/32V)			Aug-19	Sep-9	Oct-14	Nov-18	
High Voltage (1.8/5/18/24/32/40V), BCD			Aug-19		Oct-14	Nov-18	Dec-16
High Voltage (1.8/5/60/Vg1.8/5V), BCD			Aug-19		Oct-14	Nov-18	Dec-16
High Voltage, BCD (Generation-2)			Aug-19	Sep-9	Oct-14	Nov-18	Dec-16
0.25 um	55-60	Jul-1		Sep-9		Nov-11	
Logic, G (2.5/3.3V, 2.5/5V)		Jul-1 /15	Aug-12	Sep-9 /23	Oct-21	Nov-11 /18	Dec-16
Logic, G (2.5/3.3V, 2.5/5V), Embedded OTP				Sep-9		Nov-11	
Logic, G (2.5/3.3V), Embedded MTP				Sep-9		Nov-11	
Mixed-Signal/RF, G (2.5/3.3V, 2.5/5V)		Jul-1 /15	Aug-12	Sep-9 /23	Oct-21	Nov-11 /18	Dec-16
Mixed-Signal, G (2.5/3.3V, 2.5/5V), Embedded OTP				Sep-9		Nov-11	
Mixed-Signal/RF, G (2.5/3.3V), Embedded MTP				Sep-9		Nov-11	
High Voltage, BCD (2.5/5/12/24/40V/Vg2.5/5V)		Jul-15	Aug-12	Sep-23	Oct-21	Nov-18	Dec-16
High Voltage, BCD (2.5/5/12/24/40/Vg2.5/5/12V)		Jul-15	Aug-12	Sep-23	Oct-21	Nov-18	Dec-16
High Voltage, BCD (2.5/5/60V/Vg 2.5/5V)			Aug-12		Oct-21		Dec-16
High Voltage, BCD (2.5/5/12/24/40/60/Vg2.5/5/12V)			Aug-12		Oct-21		Dec-16
High Voltage, Gen-2 BCD (2.5/5/7/12/20/24/40/45/60V/Vg 2.5/5/12V)			Aug-12		Oct-21		Dec-16
0.35 um	33-51	Jul-1		Sep-9		Nov-4	
Logic, G, Polycide/Silicide (3.3/5V)		Jul-1		Sep-9		Nov-4	
Mixed-Signal, G (3.3/5V)		Jul-1		Sep-9		Nov-4	
High Voltage, G, DDD (3.3/12/13.5)		Jul-1		Sep-9		Nov-4	
High Voltage, DDD (3.3/12/13.5/15/18V)				Sep-9			
High Voltage, BCD (3.3/20/23/Vg3.3V)		Jul-1				Nov-4	
High Voltage, BCD (3.3/5/12/15/20/40/Vg3.3/5/12V)		Jul-1				Nov-4	
SiGe BiCMOS, G (3.3V)	51		Aug-26			Nov-25	
0.5 um	48			Sep-23			Dec-23
High Voltage, (5/20/450/600/700/800V)				V			V

* Длительность шаттла зависит так же от выбора дополнительных опций, для подсчета точного времени производства. Обращайтесь к нашим менеджерам!